

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
CAPPELLETTI)
)
Serial No. **Not Yet Assigned**)
)
Filing Date: **Herewith**)
)
For: **METHOD OF MAKING FLOATING GATE**)
 NON-VOLATILE MEMORY CELL WITH)
 LOW ERASING VOLTAGE)
 HAVING DOUBLE LAYER GATE)
 DIELECTRIC)
_____)

This application is a divisional of Serial No. 10/158,706 filed May 30, 2002, which is a divisional of Serial No. 09/521,876 filed March 8, 2000, now U.S. Patent No. 6,399,444, which is a divisional of Serial No. 08/940,856 filed September 29, 1997, now U.S. Patent No. 6,054,731

PRELIMINARY AMENDMENT

Mail Stop Divisional
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to the calculation of fees and examination of the present application, please enter the amendments and remarks set out below.

I HEREBY CERTIFY THIS PAPER OR FEE IS BEING DEPOSITED WITH THE U.S. POSTAL SERVICE "EXPRESS MAIL POST OFFICE TO ADDRESSEE" SERVICE UNDER 37 CFR 1.10 ON THE DATE INDICATED BELOW AND IS ADDRESSED TO: MAIL STOP DIVISIONAL, COMMISSIONER FOR PATENTS, P. O. BOX 1450, ALEXANDRIA, VA 22313-1450.

EXPRESS MAIL NO: EV 322694069 US

DATE OF DEPOSIT: March 3, 2004

NAME: Justin Goree

SIGNATURE: Justin Goree

In re Patent Application of:
CAPPELLETTI
Serial No. **Not Yet Assigned**
Filing Date: **Herewith**

In the Title:

Please delete the current title and substitute:

-- METHOD OF MAKING FLOATING GATE NON-VOLATILE MEMORY
CELL WITH LOW ERASING VOLTAGE HAVING DOUBLE LAYER GATE
DIELECTRIC --